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CHRP™ ISA Audio Device Binding

to:

IEEE 1275-1994

Standard for Boot

(Initialization, Configuration)

Firmware

Revision: 1.0 Unapproved DRAFT

Date: May 6, 1996

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## 1. Purpose of this Device Binding

This document specifies the application of Open Firmware to the PowerPC Common Hardware Reference Platform (CHRP™) ISA base audio controller, including device-specific requirements and practices for initialization, properties, and methods. This device shall be a child of an ISA or EISA Bus Node.

## 2. Revision History

Revision 1.0 Unapproved DRAFT May 6, 1996 Initial revision. Jordan Brown, Sunsoft and John Kingman, IBM editors

## 3. References

This Open Firmware System binding standard *shall* be used in conjunction with the following publications. When the following standards are superseded by an approved revision, the revision *shall* apply.

[1] *IEEE Std 1275-1994 Standard for Boot (Initialization, Configuration) Firmware, Core Practices and Requirements.*

[2] *Core Errata, IEEE P1275.7/D4.*

[3] *ISA/EISA/ISA-PnP binding to: IEEE Std 1275-1994, Standard for Boot (Initialization, Configuration) Firmware.*

[4] *Device Support Extensions to: IEEE Std 1275-1994, Standard for Boot (Initialization, Configuration) Firmware.*

[5] *PowerPC Microprocessor Common Hardware Reference Platform: I/O Device Reference.* This document describes the PowerPC Common Hardware Reference Platform (CHRP™) System Standard I/O Devices; hardware registers, register locations, and hardware attributes.

[6] *Open Firmware Recommended Practice: Generic Names.*

[7] *Open Firmware Recommended Practice: Interrupt Mapping.*

[8] *PowerPC Microprocessor Common Hardware Reference Platform binding to: IEEE Std 1275-1994, Standard for Boot (Initialization, Configuration) Firmware.*

## 4. Definition of Terms

**EISA:** Extended Industry Standard Architecture

**ISA:** Industry Standard Architecture

## 5. Device Characteristics (Informative)

The CHRP audio device supplies basic, mature function for the recording, playback, and mixing of sound, as specified in [5].

## 6. Device-specific Configuration Variables

None.



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## 7.2. Methods

### 7.2.1. Open Firmware-defined Methods for Device Nodes

As specified in [1] and [3], with the following addition or modification. This device shall have its DMA and interrupt lines “ tri-stated” when closed.

#### 7.2.1.1. Device Arguments for “Open” Method

As specified in *Device Support Extensions*[4] for “sound” devices.

### 7.2.2. Device-specific Methods for Device Nodes

As specified in *Device Support Extensions*[4] for “sound” devices.

## 8. User Interface Commands

### 8.1. Open Firmware-defined User Interface Commands

None.

### 8.2. Device-specific User Interface Commands

None.

## 9. Device State

### 9.1. Device State When Client is Started

This device shall be closed when the client is started.

### 9.2. Device State Required When Client Calls Open Firmware

For devices not selected as Open Firmware' s “ console input device” or “ console output device” device, there is no requirement. Typically, this device is unsuitable for use as an Open Firmware console input or console output device.

## 10. Other Commentary